



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yoshiyuki Yanagisawa et al.      Atty. Docket No.    075834.00085  
Serial No.: 09/ 876,290      Group Art Unit:    2827  
Filed:      June 7, 2001      Examiner:    David E. Graybill  
Title:      "ASSEMBLY JIG AND MANUFACTURING METHOD OF  
MULTILAYER SEMICONDUCTOR DEVICE"

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

S I R:

**RESPONSE TO ELECTION/RESTRICTION**

In response to the Restriction Requirement dated July 30, 2003, Applicant elects claims 1-7, of Invention I, directed to an apparatus. Applicant therefore withdraws from prosecution in the above-identified application, claims 8-10, of Invention II, directed to a process, without prejudice, but reserve herein the right to prosecute such claims in a later filed continuation or divisional application.

Applicant therefore awaits consideration and examination of elected claims 1-7 and respectfully requests consideration on the merits in due course.



Respectfully submitted,

Date: August 18, 2003

  
(Reg. #87,807)

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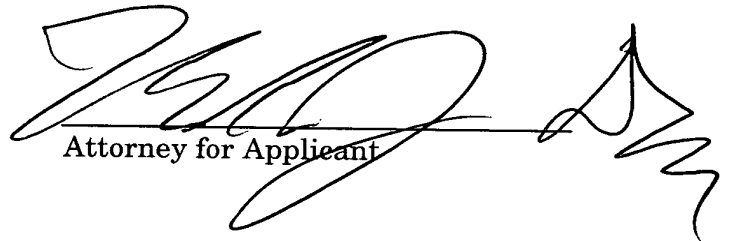


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